

ELECTRONIC CIRCUIT DESIGN

ABSTRACT OF THE DISCLOSURE

A method, system, program product and database for electronic circuit design configured to reduce data storage and computer resource requirements. The invention is binary based and leverages the redundancy of features in most electronic circuit designs by using templates of unique design features. Each template includes offset instructions for positioning the unique design feature relative to a control point rather than a previous feature. The control point is positioned within a design grid according to data in a template recall that replaces the unique design feature's instruction set in an instruction list of a design instruction file. Because each template is stored once, and only once, considerable reduction in design data size is achieved. In addition, each template recall has a fixed width data format that restrains data size and allows more efficient processing.